

Abstract Hoare Logic

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TMC, Network Algebras, and Applications

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Aim

Hoare logic for continuous systems



Outline

- 1 **Introduction**
 - Hoare logic
 - TMC and system categories
- 2 **Abstract Hoare logic**
 - Verification functor
 - Abstract logical rules
- 3 **Instantiations**
 - While programs: partial correctness
 - Pointer programs: partial correctness
 - While programs: complexity and termination
 - Stream circuits
 - Continuous systems



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Hoare Logic

Hoare triples: $\{P\} f \{Q\}$

- **Partial correctness**

If input satisfies P then output (if terminates) satisfies Q

- **Partial correctness** (pointer programs)

If input satisfies P then program does not abort and output (if terminates) satisfies Q

- **Backward reasoning**

For output to satisfy Q it is sufficient that input satisfies P

- **Total correctness**

If input satisfies P then f terminates and output satisfies Q



Hoare Logic

- **Higher order programs**
- **Parallel programs**
- ...



Hoare Logic

- **Higher order programs**
- **Parallel programs**
- ...
- **Continuous systems**



Motivation

- Develop Hoare logic for **continuous systems**



Motivation

- Develop Hoare logic for **continuous systems**
- ... or show that such thing does not exist



Motivation

- Develop Hoare logic for **continuous systems**
- ... or show that such thing does not exist
- Proceeded by trying to understand “structure” of Hoare logics

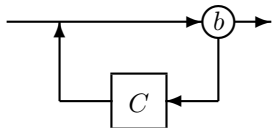
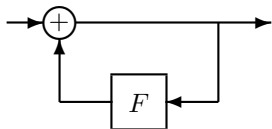


Related Work

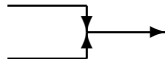
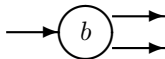
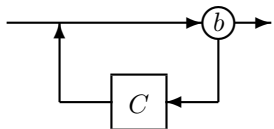
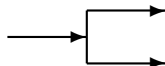
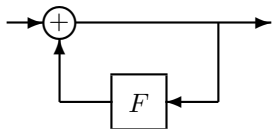
- Dijkstra's predicate transformer
- Kozen's KAT (Kleene Algebras with Test)
- Abramsky's specification categories
- Bloom and Esik's iteration theory
- ...



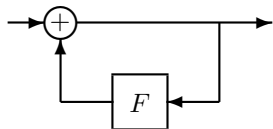
Network vs Flowcharts



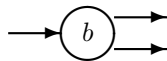
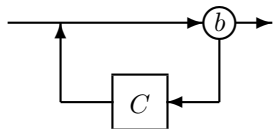
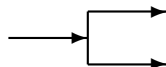
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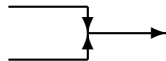
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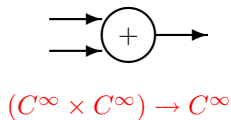
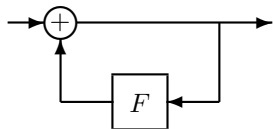
$$C^\infty \rightarrow (C^\infty \times C^\infty)$$



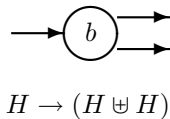
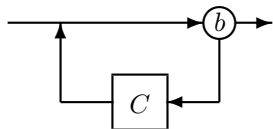
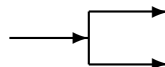
$$H \rightarrow (H \uplus H)$$



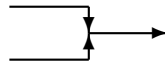
Network vs Flowcharts



$$C^\infty \rightarrow (C^\infty \times C^\infty)$$



$$(H \uplus H) \rightarrow H$$

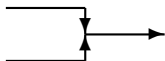
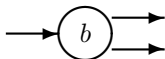


Bainbridge Duality

Exploit the duality between sum and product

$$2^{H \uplus J} \simeq 2^H \times 2^J$$

Each flowchart corresponds to a network

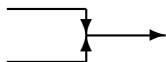
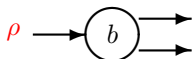


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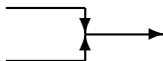
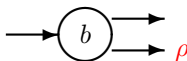


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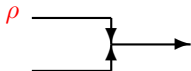
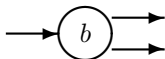


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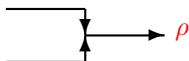
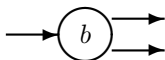


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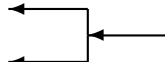
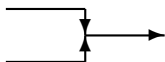
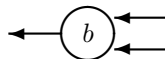
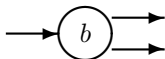


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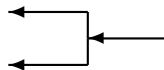
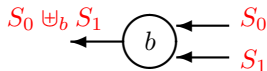
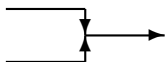
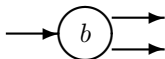


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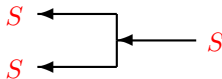
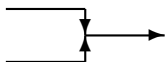
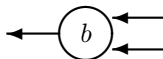
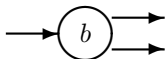


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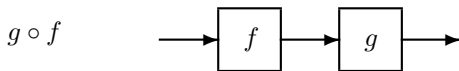
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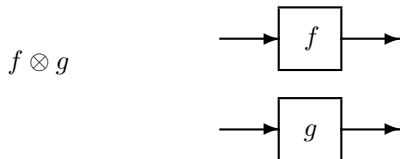


Monoidal Categories

- **Sequential composition:** categorical composition
 $f : X \rightarrow Y, g : Y \rightarrow Z$ then $g \circ f : X \rightarrow Z$



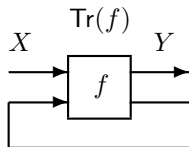
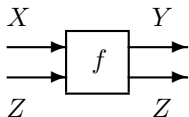
- **Parallel composition:** Monoidal operation
 $f : X \rightarrow Y, g : Z \rightarrow W$ then $f \otimes g : (X \otimes Z) \rightarrow (Y \otimes W)$



Traced Monoidal Categories

- **Iteration:** Trace operation

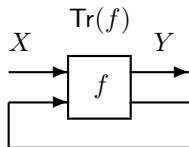
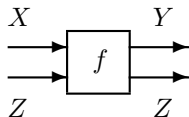
If $f : (X \otimes Z) \rightarrow (Y \otimes Z)$ then $\text{Tr}(f) : X \rightarrow Y$



Traced Monoidal Categories

- **Iteration:** Trace operation

If $f : (X \otimes Z) \rightarrow (Y \otimes Z)$ then $\text{Tr}(f) : X \rightarrow Y$



- **Examples**

- Disjoint union

$$\text{Tr}(f) \equiv \{ \langle x, y \rangle : \exists z_0, \dots, z_n (\langle x, z_0 \rangle \in f \wedge \dots \wedge \langle z_n, y \rangle \in f) \}$$

- Cartesian products

$$\text{Tr}(f) \equiv \{ \langle x, y \rangle : \exists z (\langle \langle x, z \rangle, \langle y, z \rangle \rangle \in f) \}$$



System Category

Let $\text{cl}(M)$ denote the closure of the set of morphisms M under sequential and monoidal composition, and trace.

Definition (System category)

A *system category* \mathcal{S} is a traced monoidal category with a distinguished set of morphisms $\mathcal{S}_b \subseteq \mathcal{S}_m$, so-called *basic systems*, such that $\text{cl}(\mathcal{S}_b) = \mathcal{S}_m$.



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Flowcharts

Boolean Test ($\Sigma \rightarrow \Sigma \uplus \Sigma$)

Joining of Wires ($\Sigma \uplus \Sigma \rightarrow \Sigma$)

Assignment ($\Sigma \rightarrow \Sigma$)

Stream circuits

Sum ($\Sigma \times \Sigma \rightarrow \Sigma$)

Splitting of Wires ($\Sigma \rightarrow \Sigma \times \Sigma$)

Scalar Multiplication ($\Sigma \rightarrow \Sigma$)

Register ($\Sigma \rightarrow \Sigma$)



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Hoare Logic

- *Pre/Post-conditions:*
Describe properties of input/output



Hoare Logic

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- *Ordering on information:*
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Predicate transformers



Hoare Logic

- *Pre/Post-conditions:*
Describe properties of input/output
- *Ordering on information:*
Rule of consequence
- *Partial correctness assertions:*
Predicate transformers
- *Others:*
Strongest post condition, loop invariant, ...



Pre Order

Hoare Logic	Abstract Hoare Logic
Pre/Post-conditions	Elements of pre-orders
Logical implication	Partial order
Rule of consequence	Monotonicity
Loop invariants	Fixed points
...	...



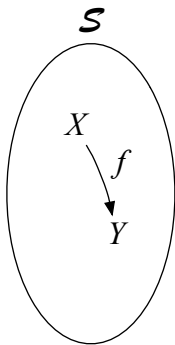
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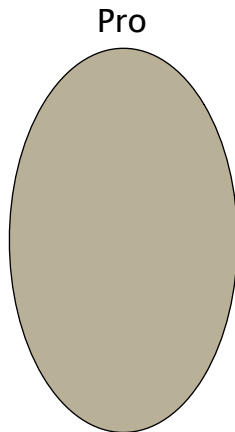
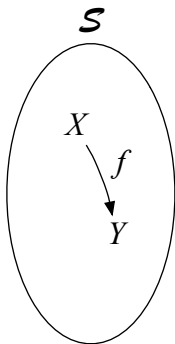
Hoare logic derived from embedding of a TMC into category of pre-orders



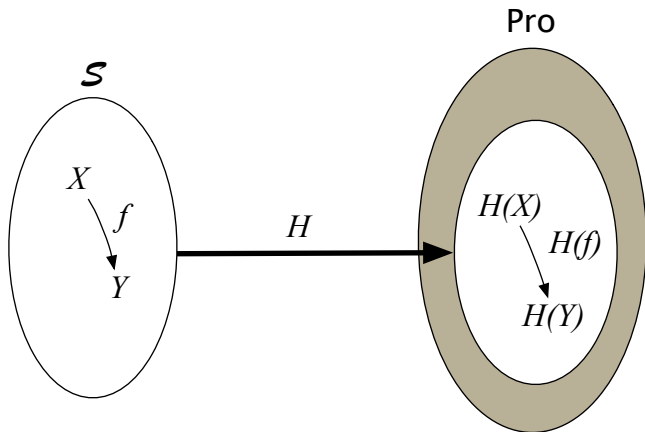
Pre Order



Pre Order



Pre Order



Verification Functor

Definition (Verification functor)

A strict monoidal functor $H : \mathcal{S} \rightarrow \text{Pro}$ is called a *verification functor* for \mathcal{S} if it satisfies:

(1) trace soundness

$$\exists Q^{H(Z)} (H(f)\langle P, Q \rangle \sqsubseteq \langle R, Q \rangle) \Rightarrow H(\text{Tr}(f))(P) \sqsubseteq R$$

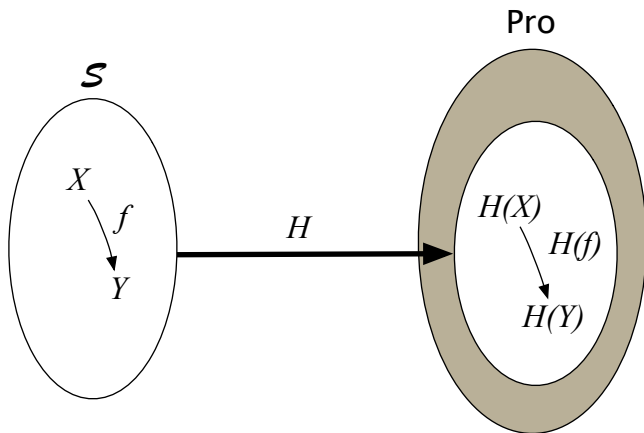
(2) trace completeness

$$H(\text{Tr}(f))(P) \sqsubseteq R \Rightarrow \exists Q^{H(Z)} (H(f)\langle P, Q \rangle \sqsubseteq \langle R, Q \rangle)$$

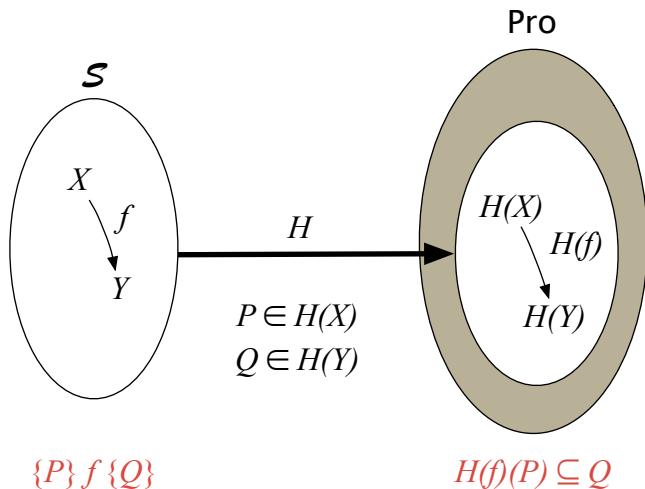
for all $f : X \otimes Z \rightarrow Y \otimes Z$ in \mathcal{S} , and $P \in H(X)$, $R \in H(Y)$.



Abstract Hoare Triples



Abstract Hoare Triples



Abstract Hoare Triples

Let

- $H : \mathcal{S} \rightarrow \text{Pro}$ be a verification functor
- $f : X \rightarrow Y$ is a morphism (system) in \mathcal{S}
- $P \in H(X)$ and $Q \in H(Y)$

Definition (Abstract Hoare triples)

We define abstract Hoare triples as

$$\{P\} f \{Q\} \equiv H(f)(P) \sqsubseteq_{H(Y)} Q$$



Abstract Hoare Logic

Theorem (Soundness and completeness)

The following set of rules is sound and complete for any system category S and verification functor $H : S \rightarrow \text{Pro}$:

$$\frac{f \in \mathcal{S}_b}{\{P\} f \{H(f)(P)\}} \text{ (axiom)}$$

$$\frac{P' \sqsubseteq_X P \quad \{P\} f \{Q\} \quad Q \sqsubseteq_Y Q'}{\{P'\} f \{Q'\}} \text{ (csq)}$$



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$$\frac{\{P\} f \{Q\} \quad \{R\} g \{S\}}{\{\langle P, R \rangle\} f \otimes g \{\langle Q, S \rangle\}} \text{ (}\otimes\text{)}$$

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$$\frac{\{P\} f \{Q\} \quad \{R\} g \{S\}}{\{\langle P, R \rangle\} f \otimes g \{\langle Q, S \rangle\}} \text{ (}\otimes\text{)} \quad \frac{\{\langle P, Q \rangle\} f \{\langle R, Q \rangle\}}{\{P\} \text{Tr}_S(f) \{R\}} \text{ (Tr}_S\text{)}$$

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While Programs

- Var: set of program variables
- Store $\Sigma : \text{Var} \rightarrow \mathbb{Z}$
- Atomic programs
 - Assignment $(x := t) : \Sigma \rightarrow \Sigma$
 - Joining $\Delta : \Sigma \uplus \Sigma \rightarrow \Sigma$
 - Boolean test $\text{if}_b : \Sigma \rightarrow \Sigma \uplus \Sigma$



While Programs (partial correctness, forward reasoning)

- Pre order $(\mathcal{P}(\Sigma), \subseteq)$
- $H(f)(P) ::= \{y \in Y : \exists x \in P (f(x) = y)\}$
 $H(f)(P) ::= \text{SPC}(f, P)$
- $\{P\} f \{Q\}$ means $H(f)(P) \subseteq Q$, i.e.
“if P holds before execution then (if program terminates) Q holds afterwards”
- For the basic systems we have:

$$\begin{array}{lll} \{P\} & x := t & \{\exists x_0 (P[x_0/x] \wedge x = t[x_0/x])\} \\ \{\langle P, Q \rangle\} & \Delta & \{P \vee Q\} \\ \{P\} & \text{if}_b & \{\langle P \wedge b, P \wedge \neg b \rangle\} \end{array}$$



While Programs (partial correctness, backward reasoning)

- Pre order $(\mathcal{P}(\Sigma), \supseteq)$
- $H(f)(P) \equiv \{x \in X : f(x) \in Q\}$
 $H(f)(P) \equiv \text{WPC}(f, P)$
- $\{P\} f \{Q\}$ means $H(f)(P) \supseteq Q$, i.e.
“in order for P to hold after (terminating) execution it is sufficient that Q holds before”
- For the basic systems we have:

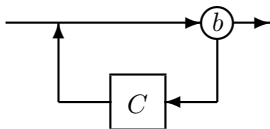
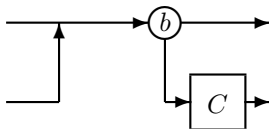
$$\{P\} \quad x := t \quad \{P[t/x]\}$$

$$\{P\} \quad \Delta \quad \{\langle P, P \rangle\}$$

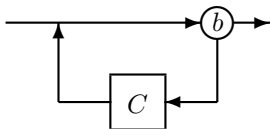
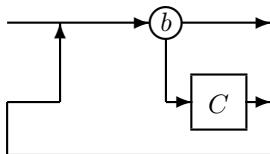
$$\{\langle P, Q \rangle\} \quad \text{if}_b \quad \{(P \wedge b) \vee (Q \wedge \neg b)\}$$



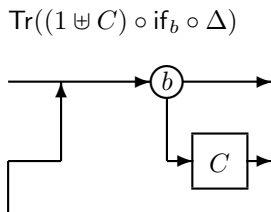
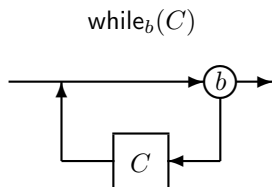
While Loop Rule

 $\text{while}_b(C)$

 $(1 \uplus C) \circ \text{if}_b \circ \Delta$


While Loop Rule

 $\text{while}_b(C)$

 $\text{Tr}((1 \uplus C) \circ \text{if}_b \circ \Delta)$


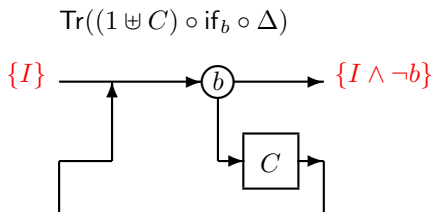
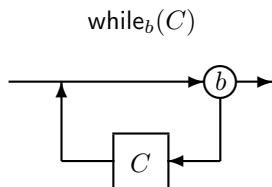
While Loop Rule



$$\begin{array}{c}
 \frac{\{I \wedge \neg b\} 1 \quad \{I \wedge \neg b\} \quad \{I \wedge b\} C \quad \{I\}}{\{I\} \text{if}_b \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad \{ \langle I \wedge \neg b, I \wedge b \rangle \} 1 \uplus C \quad \{ \langle I \wedge \neg b, I \rangle \}} \quad (\uplus) \\
 \frac{\{I\} \text{if}_b \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad \{ \langle I \wedge \neg b, I \rangle \}}{\{I\} (1 \uplus C) \circ \text{if}_b \{ \langle I \wedge \neg b, I \rangle \}} \quad (\circ) \\
 \frac{\{ \langle I, I \rangle \} (1 \uplus C) \circ \text{if}_b \circ \Delta \quad \{ \langle I \wedge \neg b, I \rangle \}}{\{I\} \text{while}_b(C) \quad \{I \wedge \neg b\}} \quad (\text{Tr})
 \end{array}$$



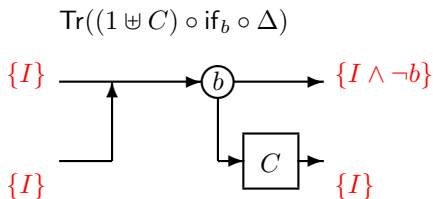
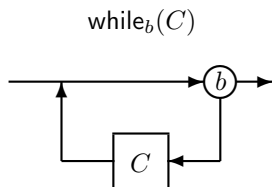
While Loop Rule



$$\begin{array}{c}
 \frac{\frac{\frac{\{I \wedge \neg b\} \ 1 \ \{I \wedge \neg b\} \ \{I \wedge b\} \ C \ \{I\}}{\{I\} \ \text{if}_b \ \{\langle I \wedge \neg b, I \wedge b \rangle\}} \ \{\langle I \wedge \neg b, I \wedge b \rangle\} \ 1 \uplus C \ \{\langle I \wedge \neg b, I \rangle\}}{\{I\} \ (1 \uplus C) \circ \text{if}_b \ \{\langle I \wedge \neg b, I \rangle\}} \ (\circ)}{\{\langle I, I \rangle\} \ (1 \uplus C) \circ \text{if}_b \circ \Delta \ \{\langle I \wedge \neg b, I \rangle\}} \ (\circ)} \\
 \hline
 \{I\} \ \text{while}_b(C) \ \{I \wedge \neg b\} \ (\text{Tr})
 \end{array}$$



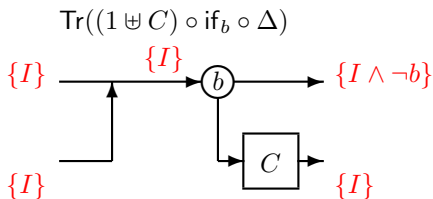
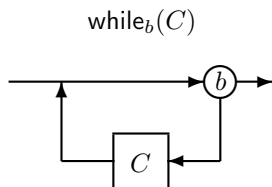
While Loop Rule



$$\begin{array}{c}
 \frac{\frac{\frac{\{I \wedge \neg b\} \quad 1 \quad \{I \wedge \neg b\} \quad \{I \wedge b\} \quad C \quad \{I\}}{\{I\} \text{if}_b \{\langle I \wedge \neg b, I \wedge b \rangle\}} \quad \{I \wedge \neg b\} \quad 1 \uplus C \quad \{\langle I \wedge \neg b, I \rangle\}}{\{I\} (1 \uplus C) \circ \text{if}_b \{\langle I \wedge \neg b, I \rangle\}} \quad (\circ)}{\{\langle I, I \rangle\} (1 \uplus C) \circ \text{if}_b \circ \Delta \quad \{\langle I \wedge \neg b, I \rangle\}} \quad (\circ)}{\{I\} \text{while}_b(C) \quad \{I \wedge \neg b\}} \quad (\text{Tr})
 \end{array}$$



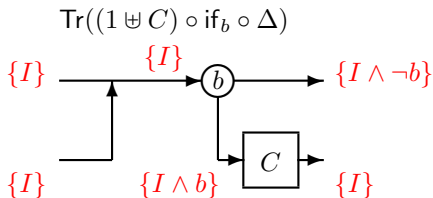
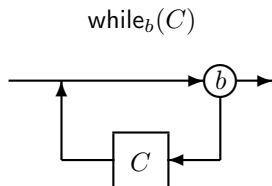
While Loop Rule



$$\begin{array}{c}
 \frac{\{I \wedge \neg b\} \quad 1 \quad \{I \wedge \neg b\} \quad \{I \wedge b\} \quad C \quad \{I\}}{\{I\} \text{if}_b \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad 1 \uplus C \quad \{ \langle I \wedge \neg b, I \rangle \}} \quad (\uplus) \\
 \frac{\{I\} \text{if}_b \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad \{ \langle I \wedge \neg b, I \rangle \}}{\{I\} (1 \uplus C) \circ \text{if}_b \{ \langle I \wedge \neg b, I \rangle \}} \quad (\circ) \\
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 \end{array}$$



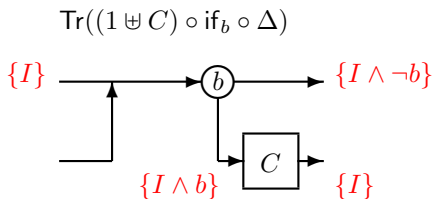
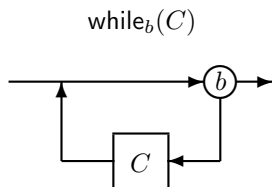
While Loop Rule



$$\begin{array}{c}
 \frac{\{I \wedge \neg b\} \quad 1 \quad \{I \wedge \neg b\} \quad \{I \wedge b\} \quad C \quad \{I\}}{\{I\} \text{if}_b \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad 1 \uplus C \quad \{ \langle I \wedge \neg b, I \rangle \}} \quad (\uplus) \\
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While Loop Rule



$$\begin{array}{c}
 \frac{\{I \wedge \neg b\} \quad 1 \quad \{I \wedge \neg b\} \quad \{I \wedge b\} \quad C \quad \{I\}}{\{I\} \text{if}_b \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad \{ \langle I \wedge \neg b, I \wedge b \rangle \} \quad 1 \uplus C \quad \{ \langle I \wedge \neg b, I \rangle \}} \quad (\uplus) \\
 \frac{\{I\} \quad (1 \uplus C) \circ \text{if}_b \quad \{ \langle I \wedge \neg b, I \rangle \}}{\{ \langle I, I \rangle \} \quad (1 \uplus C) \circ \text{if}_b \circ \Delta \quad \{ \langle I \wedge \neg b, I \rangle \}} \quad (\circ) \\
 \frac{\{ \langle I, I \rangle \} \quad (1 \uplus C) \circ \text{if}_b \circ \Delta \quad \{ \langle I \wedge \neg b, I \rangle \}}{\{I\} \text{while}_b(C) \quad \{I \wedge \neg b\}} \quad (\text{Tr})
 \end{array}$$



Pointer Programs

- Store $\Sigma : \text{Var} \rightarrow \mathbb{Z}$
- Heap $\Pi : \text{partial functions } \mathbb{N} \rightarrow \mathbb{Z} \text{ with finite domain}$
- State : $(\Sigma \times \Pi) \cup \{\text{abort}\}$
- Atomic programs
 - Look up $(x := [t])$
 - Mutation $([t] := s)$
 - Allocation $(x := \text{new}(t))$
 - Deallocation $\text{disp}(t)$



Separation Logic

- Pre order $(\mathcal{P}(\Sigma \times \Pi), \supseteq)$
- $H(f)(P) \equiv \text{WPC}(f, P)$
- $\{P\} f \{Q\}$ means $H(f)(P) \supseteq Q$, i.e.
“if Q holds before execution then f does not abort and if terminates output satisfies P ”
- For the basic systems we have:

$$\{P\} \quad x := [t] \quad \{\exists v'((t \mapsto v') * ((t \mapsto v') -* P[v'/x]))\}$$

$$\{P\} \quad [t] := s \quad \{(t \mapsto -) * ((t \mapsto s) -* P)\}$$

$$\{P\} \quad x := \text{new}(t) \quad \{\forall i((i \mapsto t) -* P[i/x])\}$$

$$\{P\} \quad \text{disp}(t) \quad \{(t \mapsto -) * P\}$$



Hoare Logic for Complexity (backward reasoning)

- Pre order ($\Sigma \rightarrow \mathbb{N}_\infty, \leq$)
- $H(f)(P)(\rho) \equiv P(f(\rho)) + (\text{time to execute } f \text{ on } \rho)$
- $\{P\} f \{Q\}$ means $\forall \rho (H(f)(P)(\rho) \leq Q(\rho))$
“starting with Q credits we can run f and still have P left”
- For the basic systems we have:

$$\begin{array}{lcl} \{P\} & x := t & \{P[t] + 1\} \\ \{P\} & \Delta & \{\langle P + 1, P + 1 \rangle\} \\ \{\langle P, Q \rangle\} & \text{if}_b & \{\max\{P, Q\} + 1\} \end{array}$$

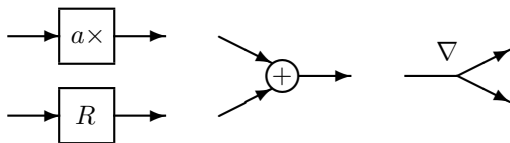


Stream Circuits

Smooth functions can be represented as *streams* \mathbb{R}^ω

$$\sigma_y = [y(0), y'(0), y''(0), \dots]$$

Stream circuits basic operations:

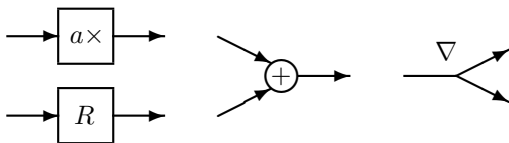


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Stream circuits basic operations:



$$y' - y = u$$

$$y(0) = 0$$

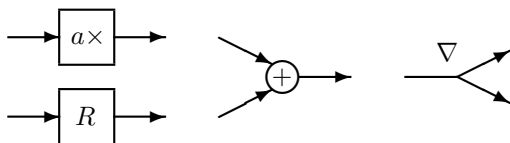


Stream Circuits

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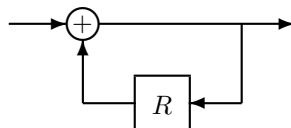
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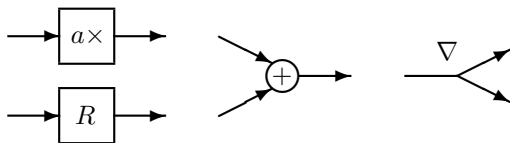


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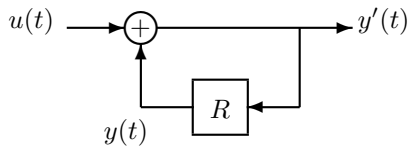
$$\sigma_y = [y(0), y'(0), y''(0), \dots]$$

Stream circuits basic operations:



$$y' - y = u$$

$$y(0) = 0$$



Hoare Logic for Stream Circuits

- Pre order $(\mathbb{R}^\omega, =)$
- $H(f)(P) :\equiv f(P)$
- $\{P\} f \{Q\}$ means $f\langle P, Q \rangle$
“input P is related to output Q ”
- For the basic systems we have:

$$\begin{array}{lcl} \{P\} & a \times & \{aP\} \\ \{\langle P, Q \rangle\} & (+) & \{P + Q\} \\ \{P\} & \nabla & \{\langle P, P \rangle\} \\ \{P\} & R & \{0 * P\} \end{array}$$



Hoare Logic for Continuous Systems

$$\frac{f \in \mathcal{S}_b}{\{P\} f \{f(P)\}} \text{ (axiom)}$$

$$\frac{\{P\} f \{Q\} \quad \{Q\} g \{R\}}{\{P\} g \circ f \{R\}} \text{ (}\circ\text{)}$$

$$\frac{\{P\} f \{Q\} \quad \{R\} g \{S\}}{\{\langle P, R \rangle\} f \otimes g \{\langle Q, S \rangle\}} \text{ (}\otimes\text{)}$$

$$\frac{\{\langle P, Q \rangle\} f \{\langle R, Q \rangle\}}{\{P\} \text{Tr}_S(f) \{R\}} \text{ (Tr}_S\text{)}$$

$$\frac{P' = P \quad \{P\} f \{Q\} \quad Q = Q'}{\{P'\} f \{Q'\}} \text{ (csq)}$$



Hoare Logic for Continuous Systems

$$\frac{f \in \mathcal{S}_b}{\{P\} f \{f(P)\}} \text{ (axiom)}$$

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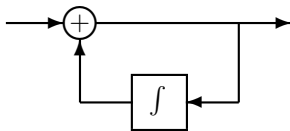
$$\frac{P' = P \quad \{P\} f \{Q\} \quad Q = Q'}{\{P'\} f \{Q'\}} \text{ (csq)}$$

Hoare logic translates networks into (differential) equations!



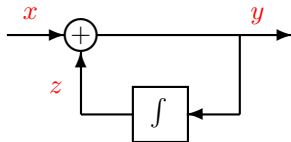
Example

$$\text{Tr}(\langle 1, f \rangle \circ \nabla \circ (+))$$



Example

$$\text{Tr}(\langle 1, f \rangle \circ \nabla \circ (+))$$



$$\frac{\frac{\frac{\{x + z\} \nabla \{\langle x + z, x + z \rangle\} \{\langle x + z, x + z \rangle\} \langle 1, f \rangle \{\langle y, z \rangle\}}{\{x + z\} \langle 1, f \rangle \circ \nabla \{\langle y, z \rangle\}} \quad (\circ)}{\{\langle x, z \rangle\} \langle 1, f \rangle \circ \nabla \circ (+) \{\langle y, z \rangle\}} \quad (\text{Tr})}{\{x\} \text{Tr}(\langle 1, f \rangle \circ \nabla \circ (+)) \{y\}} \quad (\circ)$$

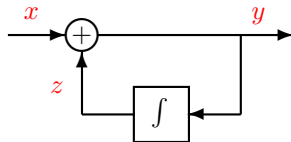


Example

$$\text{Tr}(\langle 1, f \rangle \circ \nabla \circ (+))$$

$$y = x + z$$

$$f(x + z) = z$$



$$\frac{\frac{\frac{\{x + z\} \nabla \{\langle x + z, x + z \rangle\} \quad \frac{\{x + z\} \langle 1, \int \rangle \{\langle y, z \rangle\}}{\{x + z\} \langle 1, \int \rangle \circ \nabla \{\langle y, z \rangle\}} \quad (\circ)}{\{x + z\} \langle 1, \int \rangle \circ \nabla \{\langle y, z \rangle\}} \quad (\circ)}{\{x, z\} \langle 1, \int \rangle \circ \nabla \circ (+) \{\langle y, z \rangle\}} \quad (\text{Tr})}{\{x\} \text{Tr}(\langle 1, \int \rangle \circ \nabla \circ (+)) \{y\}} \quad (\circ)$$



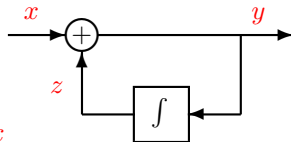
Example

$$\text{Tr}(\langle 1, f \rangle \circ \nabla \circ (+))$$

$$y = x + z$$

$$f(x + z) = z$$

$$\Rightarrow f y = y - x$$



$$\frac{\frac{\{x + z\} \ 1 \ \{y\} \quad \{x + z\} \ \int \ \{z\}}{\{x + z\} \ \nabla \ \{\langle x + z, x + z \rangle\} \ \{\langle x + z, x + z \rangle\} \ \langle 1, \int \rangle \ \{\langle y, z \rangle\}} \quad (\circ)}{\frac{\{x + z\} \ \langle 1, \int \rangle \ \circ \ \nabla \ \{\langle y, z \rangle\}}{\{\langle x, z \rangle\} \ \langle 1, \int \rangle \ \circ \ \nabla \ \circ \ (+) \ \{\langle y, z \rangle\}} \quad (\text{Tr})} \{x\} \ \text{Tr}(\langle 1, \int \rangle \ \circ \ \nabla \ \circ \ (+)) \ \{y\}}$$



Summary

- Abstraction of Hoare logic
 - Flowcharts programs
 - Pointer programs
 - ...
- *Future work:*
 - Total correctness
 - Higher order programs
 - Concurrency
- Continuous systems
 - Finding loop invariants = solving differential equations
- *Future work:*
 - Use modularity to partially solve diff equations
 - Reason with black boxes
 - Non linear systems

